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TITLE: CODE TRANSMITTER AND METHOD

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ABSTRACT:

PURPOSE: To simplify the address generation procedure by decreasing a memory access frequency for generating an external code and an internal code in the code transmitter in which an error check correction code comprising a product code is added to a code and the resulting code is sent.

CONSTITUTION: An external code coding circuit 18 generates a check point of an external code of an information data from a memory 14 and writes it to a memory 22, and simultaneously, an internal code coding circuit 20 generates a check point of an internal code of an information data from which the check point of the external code is eliminated and stores in the memory

24. An inner code coding circuit 26 reads the storage data in the memories 22, 24 to generate a check point of the inner code in which the external code check point is added. A selection circuit 28 outputs the information data, the check point of the external code and the check point of the internal code according to a prescribed sequence.

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